



**RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE REQUESTED
EXAMINING GROUP 2133**

PATENT

Customer No. 22,852

Attorney Docket No. 04329.2762-00

*AF-8
JFW*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Yasuyuki NOZUYAMA

Application No.: 10/091,552

Filed: March 7, 2002

For: SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
AND TEST METHOD THEREOF

)
)
) Group Art Unit: 2133

) Examiner: Joseph D. TORRES

) Confirmation No.: 9081

) Mail Stop AF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT AFTER FINAL

In reply to the Final Office Action mailed June 8, 2005, the period for response having been extended to October 8, 2005 by a request for extension of one month and fee payment filed concurrently herewith, and pursuant to 37 C.F.R. § 1.116, Applicant proposes that this application be amended as follows:

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with RCE X
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V.S.
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Amendments to the Claims are reflected in the listing of claims in this paper.

Remarks/Conclusion follow the amendment section of this paper.

*Do not enter
11/7/2005*